

## THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.

7,057,981

Confirmation No. 2010

Issue Date

June 6, 2006

Applicant :

A. KANO, et al

Appl. No.

10/766,015

Filed

January 29, 2004

Title

DISK ARRAY SYSTEM AND METHOD FOR CONTROLLING DISK ARRAY SYSTEM

Docket No. :

500.43451X00

Customer No.:

24956

Director of the U.S. Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450

## REQUEST FOR CERTIFICATE OF CORRECTION APPLICANTS' MISTAKE (37 CFR §1.323)

Sir:

Enclosed is a completed Certificate of Correction form. Please issue a Certificate of Correction for the underscored material.

Pursuant to MPEP §1481, it is submitted that the mistake is either:

- (1) of a clerical nature,
- (2) of a typographical nature, or
- (3) a mistake of minor character.

Furthermore, it is submitted that the correction does not involve changes which would:

(1) constitute new matter, or

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Patent No. 7,057,981 Certificate of Correction dated June 21, 2006

(2) require reexamination.

As clearly shown in Fig. 9, switch 902 is connected between two converters 901 to connect one converter to a corresponding hard disk drive 51. Therefore, the switches 902 are clearly circuits for "selecting one converter 901" as opposed to circuits for "selecting one hard disk drive 51", as should have been stated in column 8, lines 60-61 of the patent. Since this mistake is of a minor nature and does not constitute new matter or require reexamination, it is requested that a Certificate of Correction be issued. A similar amendment has been made in all of the pending related applications.

Since the Certificate of Correction is necessitated by a mistake on the part of the Applicants, in accordance with 37 CFR §1.20(a) our Credit Card Payment Form in the amount of \$100.00 is attached in payment of the appropriate fee.

The Commissioner is hereby authorized to charge additional payment due or to credit any overpayment, to Deposit Account No. 50-1417.

Respectfully submitted,

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500.43451X00

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.:

7,057,981

DATED

June 6, 2006

INVENTOR(S):

A.KANO et al

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

ON THE TITLE PAGE

Column 8, lines 55, through column 9, line 4, please amend the paragraph as follows:

Each converter 901 is a circuit for converting a fiber channel protocol and a serial ATA protocol into each other in the same manner as the converter 801 in Fig. 8. The converter 901 is connected to a PBC 602 of an FC-AL 506. A plurality of hard disk drives 51 are connected to each converter 901 through switches 902 respectively. The switches 902 are circuits for selecting one hard disk drive 51 converter 901 with which communication should be established when the hard disk drives 51 are connected to a plurality of converters 901. Each switch 902 is provided in each disk drive unit 52. Each converter 901 is formed out of one chip or a plurality of circuits in which a protocol converting function is incorporated. For example, the converter 901 can be implemented by the configuration of an SATA master device disclosed in "US Patent Application Publication No. 2003/0135577". The converter 901 is mounted on the controller board 59, the power supply controller board 56 or the like.

Mailing Address of Sender:

Patent No. 7.057,981

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